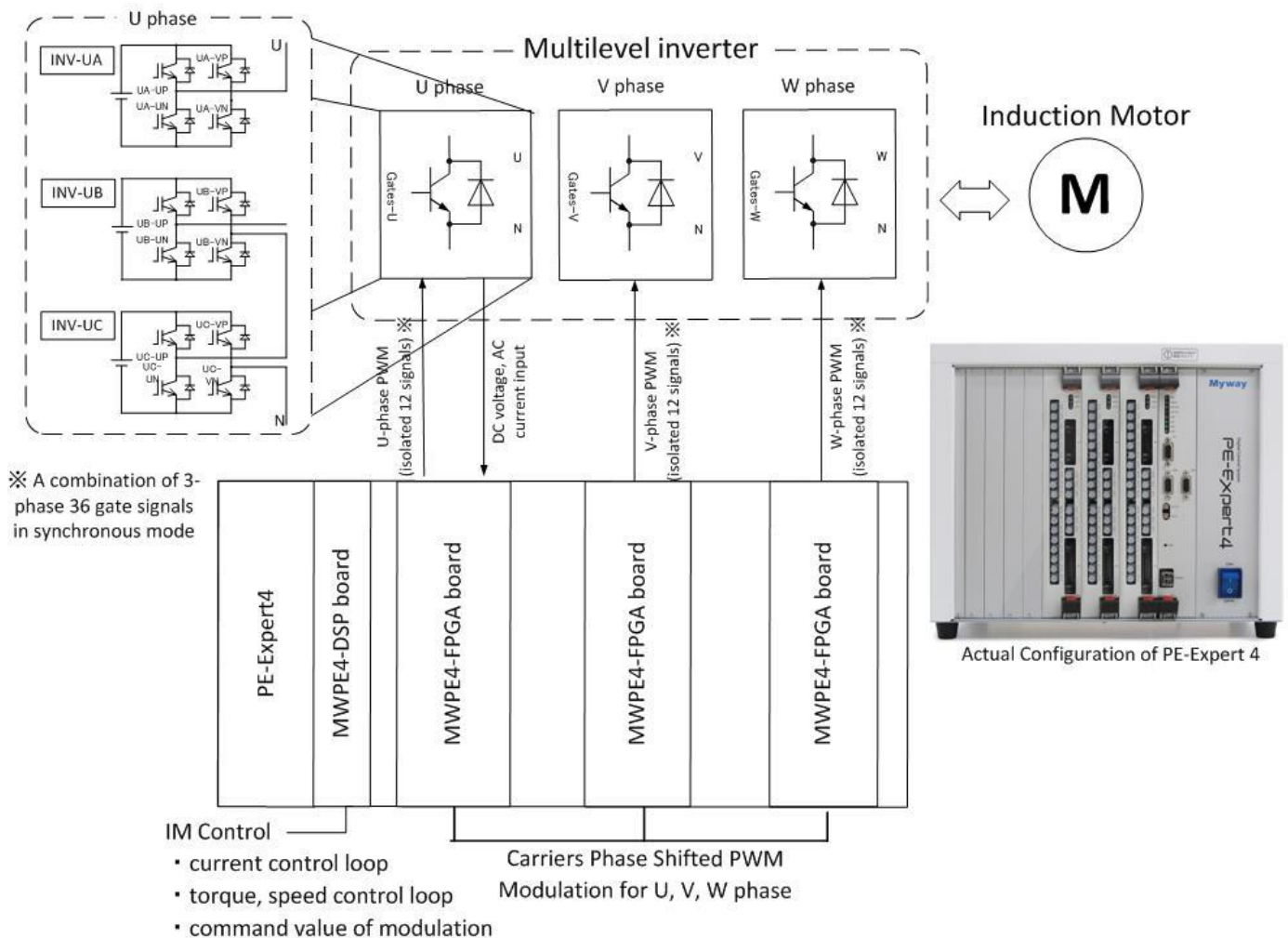


Title: Operation of Multilevel Inverter and Induction Motor

An example involving multilevel inverter and induction motor. The FPGA board will send Carrier Phase Shift PWM gate signals to IPM while the control of IM will be carried out through DSP. Users can write and download their own logic (For example Carrier Phase Shift PWM Modulation) into the FPGA board through Xilinx ISE WebPACK. Functions such as motor current vector control, torque and speed control will be written into the DSP program through PE-View X. The users may then reprogrammed the FPGA modulation to fit other multilevel inverters.



Product Information

Product Name	Model No	Main Specifications
PE-Expert 4 Rack	MWPE4-RACK12	
PE-Expert 4 DSP Board	MWPE4-C6657	DSP TMS320C6657 (1.25GHz)
PE-Expert 4 FPGA	MWPE4-FPGA24	24ch PWM, 8ch AD, 14bit